Accessing the transport properties of graphene and its multilayers at high carrier density

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We present a comparative study of high carrier density transport in mono-, bi-, and trilayer graphene using electric double-layer transistors to continuously tune the carrier density up to values exceeding $10^{14}$ cm$^{-2}$. Whereas in monolayer the conductivity saturates, in bi- and trilayer filling of the higher-energy bands is observed to cause a nonmonotonic behavior of the conductivity and a large increase in the quantum capacitance. These systematic trends not only show how the intrinsic high-density transport properties of graphene can be accessed by field effect, but also demonstrate the robustness of ion-gated graphene, which is crucial for possible future applications.

The investigation of transport through graphene layers has been focusing almost exclusively on the low carrier density regime ($n \sim 10^{12}$ cm$^{-2}$), where electrons behave as chiral particles and unexpected physical phenomena occur (1, 2). Despite exciting theoretical predictions (possible occurrence of superconductivity; refs. 3–5) and its clear relevance for technological applications (transparent electrodes for flat panel displays, ref. 6, supercapacitors, ref. 7, and biosensors, ref. 8), the high carrier density regime ($n \sim 10^{14}$ cm$^{-2}$) has remained vastly unexplored due to the limited amount of carrier density accessible in conventional solid-state field-effect transistors (9, 10). The recent development of so-called ionic-liquid gates, in which the coupling between gate-electrode and transistor channel is effectively realized through moving ions that form an electric double layer (EDL) at the liquid/channel interface (Fig. 1A), is now changing the situation. The gate voltage applied—up to several volts—drops across a very large geometrical EDL capacitance of approximately 1-nm thick. As a result, the induced carrier density can easily exceed $n_{2D} \approx 10^{14}$ cm$^{-2}$, more than one order of magnitude larger than that in conventional solid-state field-effect transistors (FETs). Such a very strong field effect is valuable for technological applications (for instance, in organic FETs, ref. 11, where it enables low-voltage operation) and as a versatile and effective tool to tune electronic states in a rich variety of systems (by modulating metal insulator transition, ref. 12, magnetoresistance, ref. 13, and by inducing superconductivity at the surface of insulators, refs. 14 and 15).

Recent works show that ion gating can also be used in combination with graphene. Experiments (e.g., Raman spectroscopy, ref. 16, quantum capacitance, ref. 17, transport, refs. 18 and 19, etc.) have focused almost exclusively on properties of monolayer, but no characteristic high carrier density features in the transport properties were identified. Here, as an efficient strategy to reveal these characteristic features, we perform a comparative study of transport in ion-gated mono-, bi-, and trilayer graphene at high carrier density of approximately $10^{14}$ cm$^{-2}$. The motivation for this strategy is twofold. First, when $n_{2D}$ exceeds values of several $10^{13}$ cm$^{-2}$, differences between monolayer and bi-/trilayer are expected, because in the latter systems the higher-energy split-off bands start to be populated, which can provide an effective way to identify signatures of the intrinsic properties characteristic of the layers of different thickness. Second, in bi- and trilayer, the regime in which higher-energy bands are populated has not yet been studied, and it is currently unknown how the opening of parallel transport channels affects the conductivity of these systems. As we now proceed to discuss, our strategy enables the observation of conductivity saturation in monolayer graphene, as well as a nonmonotonic density dependence of the conductivity in bi- and trilayer graphene, and provides the information necessary to determine the origin of these phenomena. Although a precise quantitative analysis of our data in terms of theoretical calculations capturing all the details of the graphene systems investigated is beyond present capabilities—because of the microscopic complexity of the graphene/ionic-liquid interface—we also discuss a simple model which captures the important qualitative features of our data and reproduces the correct order of magnitude of the measured quantities.

Experiments

Mono-, bi-, and trilayer graphene devices were fabricated on SiO$_2$/Si substrates by exfoliating graphite (see Materials and Methods and Fig. S1) (9). A small droplet of ionic liquid was applied onto the devices, covering the graphene layer, the gate, and a quasi-reference electrode, as shown in Fig. 1B and C. The droplet can be easily removed and substituted with a different ionic liquid, enabling the comparison of electric double-layer transistors (EDLTs) realized on the same graphene layer, with different ionic liquids (see Materials and Methods and Fig. S2A). This is important to check that the features observed in the experiments are not artifacts caused by the specific ionic liquid chosen. For all devices, the longitudinal sheet resistivity $\rho_{xx}$ and Hall coefficient $R_H$ were simultaneously measured at room temperature, in a Hall bar configuration, as a function of $V_{G} = V_{ref}$. $V_{G}$ is the voltage applied on the Pt gate electrode and $V_{ref}$ is the voltage measured on the quasi-reference electrode as shown in Fig. 1B. We found $V_{ref} \approx 0$ over the whole sweep range of $V_{G}$ (see Fig. S2B), which ensured almost all the applied $V_{G}$ dropped at the liquid/graphene interface. The measurements were performed in a limited gate voltage range, to avoid the occurrence of chemical reactions between the ionic liquid and graphene, as it is necessary to obtain reproducible and reversible results (Fig. S3 and S4). Despite this limitation, charge density as large as $n_{2D} \approx 2 \times 10^{14}$ cm$^{-2}$ could be reached.


The authors declare no conflict of interest.

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of $\Delta V_G \approx \pm 1$ V from the charge neutrality point. For larger (in modulus) $V_G$, $\sigma_{3D}$ exhibits a pronounced saturation (20). The onset of a trend toward conductivity saturation is normally seen in conventional SiO$_2$-based monolayer graphene FETs with sufficient high mobility (21). Here, the use of EDLTs makes the phenomenon unambiguously clear, owing to the much larger carrier density range spanned. The behavior of bi- and trilayer graphene devices (see Fig. 2 B and C) differs from that of monolayer. In particular, the linear increases of $\sigma_{3D}$ appear within a narrower voltage range of $\Delta V_G \approx \pm 0.5$ V near the charge neutrality point. More distinctly, outside this range, the $\sigma_{3D}$ exhibits a nonmonotonic behavior—both for electrons and holes—before continuing to increase further at higher $V_G$.

Because charge accumulation in graphene EDLT devices is not simply described by a geometrical capacitance (see below), it is necessary to determine the electron density as a function of gate voltage independently, in order to interpret the conductivity data. To this end, we have simultaneously measured the Hall resistance as a function of $V_G$. The blue lines in Fig. 2 D–F display the sheet carrier density $n_{3D}$, derived for devices fabricated on layers of different thickness. As expected, at the value of $V_G$ corresponding to the conductivity minimum $n_{3D}$ changes sign, confirming the shift of Fermi level $E_F$ across the charge neutrality point (Fig. S3). Using the carrier density determined from Hall measurements, we can directly extract the mobility of the devices. We find maximum values of 5.5, 3.5, and $9 \times 10^3$ cm$^2$/Vs close to the neutrality point, for mono-, bi-, and trilayer graphene, respectively; similar mobility values are also observed using other ionic liquid, for instance $N,N$-diethyl-$N$-(2-methoxyethyl)-N-methylammonium bis-(trifluoromethylsulfonyl)-imide (DEME-TFSI) (Fig. S5).

Having determined the density of carriers, we can directly extract the total capacitance of the devices, defined as $C = e^2/m$, from the data shown in Fig. 2 D–F. A strong asymmetry between electrons and holes is clearly present, in sharp contrast to what happens in conventional transistors based on solid dielectrics. As electron-hole symmetry is known to approximately hold in graphene, on the studied energy scale, we attribute the asymmetries observed to the properties of the EDLs. In large part, they originate from the different size of the positive and negative ions forming the ionic liquid, which are responsible for different thickness of the EDL for opposite polarities of the gate voltage. Indeed, the details of the asymmetry are different for different ionic liquids, as can be seen in Fig. 3d where the characteristics of a bilayer graphene device realized using ABIM-TFSI, DEME-TFSI, and 1-ethyl-3-methylimidazolium tetrafluoroborate (EMIM-BF$_4$) are shown. More importantly, however, the devices based on different liquids exhibit a fairly good agreement in the main features of the conductivity, including the absolute values of conductivity, the nonmonotonic behavior, and the position of the features as a function of carrier density. This observation is important because it indicates that the features in the conductivity are intrinsic to graphene.

It is apparent from Fig. 2 that the capacitance depends very strongly on $V_G$, because the total capacitance $C$ measured experimentally is given by $C = C_D + C_{G0}$ where $C_D$ is the “geometrical” capacitance of the EDL, and $C_{G0}$ is the so-called quantum capacitance associated to the finite density of states of graphene. Owing to the large geometrical capacitance of EDLs (several tens of $\mu$F/cm$^2$) (17), $C_{G0}$ dominates the total capacitance, which is why the position of the Fermi energy $E_F$ can be tuned by applying only small values of $V_G$. (In conventional graphene FETs with solid gate dielectrics, except close to the Dirac point, the quantum capacitance is normally negligible, because the geometrical capacitance is three orders of magnitude smaller than for EDLs—typically, for a 300-nm SiO$_2$ layer, ca. 12 nF/cm$^2$.) The dominant role of the quantum capacitance naturally explains why the amount of carrier density accumulated in the graphene
The interpretation of the conductivity anomalies in terms of interband scattering is further supported by the observed behavior of the capacitance of bi- and trilayer graphene (Fig. 2 E and F), which exhibits a sharp increase concomitant with the conductivity anomalies. This increase is precisely what should be expected, because when the Fermi level enters the $B_{2\pm}$ or $T_{2\pm}$ bands, the density of states increase in a step-like manner and so does the quantum capacitance (in the experiments, the step-like increase is broadened by disorder). Finally, Fig. 3B shows the relation between $n_{2\pm}$ and $V_G$, from which we find that the carrier density for the trilayer closely matches the sum of the densities in the mono- and bilayer. Such a relation is expected to approximately hold from the theoretical band structure of these systems, because the two lowest energy bands in trilayer correspond approximately to the linear band of monolayer and the lowest energy quadratic band of bilayer (SI Theoretical simulation and quantum capacitance).

**Discussion and Conclusion**

To substantiate all the considerations just made, we have analyzed the high-density electronic properties of mono-, bi-, and trilayer graphene in terms of a simple, well-defined theoretical model (20). As our only goal at this stage is to show that all the main features observed in the conductivity and capacitance measurements naturally arise from a formally correct theoretical framework, for simplicity we have included only the effect of weak short-range scattering (20) and adopted a self-consistent Born approximation scheme (23, 24). For a quantitative analysis, other scattering mechanisms should be included as well—e.g., to capture the effect of long-range Coulomb potential (25), resonant scattering (26, 27), as well as ripples (28). However, such a precise quantitative analysis is far from straightforward, because the

**Fig. 2.** Electrical properties of mono-, bi-, and trilayer graphene EDLT devices. (A–C) The conductivity $\sigma_{2\alpha}$ of mono-, bi-, and trilayer graphene, respectively, measured as a function of applied gate voltage $V_G$, in devices using ABIM-TFSI ionic liquid gate. The black dashed lines indicate the linear $V_G$ dependence of the $\sigma_{2\alpha}$ near the charge neutrality point (whose position is marked by the vertical white lines). For the monolayer (A), $\sigma_{2\alpha}$ exhibits a very pronounced saturation with increasing $V_G$ (i.e., carrier density). The saturation is absent in the bi- and trilayer, which instead show a nonmonotonic dependence of $\sigma_{2\alpha}$. In all cases, the asymmetry between positive and negative voltage is associated with the properties of the ionic liquid, and it is in large part due to the fact that positive and negative ions have different size (leading to different thicknesses of the EDL; see also Fig. 3 and Fig. S3). (D–F) Hall effect measurements of the carrier density $n_{2\alpha}$ for the graphene layers of different thickness. Three curves (overlapped) measured by fixing magnetic field while scanning $V_G$, and the pink dots measured by fixing $V_G$ while scanning the magnetic field show perfectly consistent data (see also Fig. S3). D–F also show the capacitance $C$ of the layers, obtained by deriving the accumulated $n_{2\alpha}$ with respect to $V_G$. In E and F, the vertical arrows show that the features observed in the conductivity occur at the same gate voltage as the features in the measured capacitance. This result is expected because these features indicate the starting of carrier accumulation in the higher-energy bands of bi- and trilayer. The insets of A–C illustrate schematically the main aspects of the band structure of mono-, bi-, and trilayer graphene, respectively. The green shaded areas illustrate the range within which the Fermi level can be shifted in the three cases.
TSFI as ionic liquid. The dashed black line is the sum of neutrality point) for mono-, bi-, and trilayer graphene devices, using DEME-EMIM-BF4, ABIM-TFSI, and DEEM-TFSI. The inset illustrates the maximum value of the two different polarities (owing to the different physical size of positive and negative ions). The inset illustrates the maximum value of $n_{2D}$ for electrons (white arrows) and for holes (black arrows) accessible by means of the different ionic liquids. The largest carrier density that has been accessed in this study is $2 \times 10^{14}$ cm$^{-2}$ using DEME-TFSI. (B) The dependence of carrier density $n_{2D}$ on gate voltage $V_g$ (measured from the charge neutrality point) for mono-, bi-, and trilayer graphene devices, using DEME-TFSI as ionic liquid. The dashed black line is the sum of $n_{2D}$ of mono- and bilayer graphene, which compares well to the $n_{2D}$ measured in the trilayer. This relationship is approximately expected from the known band structure of these materials, because the two lowest energy bands of trilayer roughly correspond to the lowest energy band of mono- and bilayer.

Fig. 3. Comparison of different ionic liquids. (A) The conductivity $\sigma_{2D}$ of bilayer graphene devices fabricated using three different ionic liquids (green, ABIM-TFSI; blue, DEME-TFSI; red, EMIM-BF4) as a function of carrier density $n_{2D}$ (measured from the Hall effect). It is apparent that all the main features observed in the conductivity (linear increase at low $n_{2D}$, nonmonotonic behavior at intermediate $n_{2D}$, and increase at a very large $n_{2D}$) are essentially identical, irrespective of the specific ionic liquid used. This observation indicates that these features are intrinsic properties of bilayer graphene, not influenced by the specific ionic liquid used. Similar experiments were performed on mono- and trilayer and showed a similar insensitivity to the ionic liquid. Note also that, when plotted as a function of $n_{2D}$, the asymmetry between electron and hole side in the conductivity appears to be much smaller than in the plots as a function of gate voltage (see Fig. 2). This result is indeed what should be expected if the asymmetry originates from the geometrical capacitance of the EDL that is different for the two different polarities (owing to the different physical size of positive and negative ions). The inset illustrates the maximum value of $n_{2D}$ for electrons (white arrows) and for holes (black arrows) accessible by means of the different ionic liquids. The largest carrier density that has been accessed in this study is $2 \times 10^{14}$ cm$^{-2}$ using DEME-TFSI. (B) The dependence of carrier density $n_{2D}$ on gate voltage $V_g$ (measured from the charge neutrality point) for mono-, bi-, and trilayer graphene devices, using DEME-TFSI as ionic liquid. The dashed black line is the sum of $n_{2D}$ of mono- and bilayer graphene, which compares well to the $n_{2D}$ measured in the trilayer. This relationship is approximately expected from the known band structure of these materials, because the two lowest energy bands of trilayer roughly correspond to the lowest energy band of mono- and bilayer.

graphene layers are in close proximity (approximately 1-nm distance) with very high-density molecular ions in the ionic-liquid gate. These nearby charges generate large electrostatic potential fluctuations—much larger in the case of SiO$_2$ substrates—and are certainly expected to smear all sharp features that are calculated using models where disorder has limited amplitude. Because of these sources of uncertainty, the model is only expected to give a qualitative picture of the charge accumulation in graphene (multi)layers. Indeed, on the experimental side, the broad features observed in the data prevent a precise quantitative determination of the absolute values of parameters, such as the carrier density required for the band filling. It is clear that the experimental data, although having the correct orders of magnitude, can certainly be affected by a large uncertainty at the quantitative level.

The experimental data for devices realized using ABIM-TFSI are compared with theory in Fig. 4. At a qualitative level, theory reproduces the trends seen in both $\sigma_{2D}$ vs. $n_{2D}$ and $C$ vs. $n_{2D}$ curves, for the graphene layers of different thickness. This consistency includes the trend toward conductivity saturation in the monolayer, and the features in the conductivity and in the capacitance in bi- and trilayer. In particular, the nonmonotonic behavior of the calculated conductivity in bi- and trilayer can be traced back to the presence of interband scattering, which agrees with our initial interpretation and confirms the relevance of this process at high carrier density. Note, finally, that in the model, we have not included the modification of the band structure due to the perpendicular electric field generated by the gate (i.e., the opening of a gap in bilayer, ref. 29, and the modification of the band overlap in trilayer, ref. 30). Inclusion of these effects is important for a more quantitative analysis (in particular, to achieve a more precise quantitative agreement for the values of carrier...
density at which higher-energy bands are populated in bi- and trilayer). Nevertheless their omission does not change the qualitative trends and most of the quantitative discrepancies between model and calculations originate from the very large potential fluctuations associated with the discreteness and random position of the nearby molecular ions in the ionic-liquid gate.

From the results obtained, we conclude that ionic-liquid gating is an effective and reliable technique to accumulate very large amounts of carriers in graphene-based materials. Although our investigations have mainly focused on basic aspects of the electronic properties that can be directly related to the graphene band structure, the technique has the potential to have a much broader impact, both to disclose phenomena of fundamental interest and for possible future applications. For instance, the carrier densities that have been achieved in this study are not far from those for which superconductivity is observed in graphene intercalated compounds (e.g., KC$_8$ corresponding to a carrier density of $5 \times 10^{14}$ cm$^{-2}$) and, indeed, superconductivity in graphene at high carrier density has been predicted theoretically (3–5). Accessing these higher carrier densities appears to be within experimental reach, by biasing the gate at lower temperature (approximately 220 K, just above the freezing point of the ionic liquid) to prevent the occurrence of chemical reaction (whose rate decreases exponentially with lowering temperature) over a much broader gate voltage range (15, 31).

For applications, ionic gating allows to boost the conductivity of graphene, as it is required, for instance, for the practical realization of transparent electrodes in flat panel displays (6). (Indeed, it is believed that graphene has the potential to replace the commonly used— and expensive—indium tin oxide; note that the ionic liquids themselves are also transparent.) The use of liquid gates is also much cheaper than vacuum or other gated systems. The opposite behavior with lowering temperature and the lack of gate hysteresis are the long sought signs of a high-quality interface and an efficient trapping of carriers by the liquid, which would result in irreversible device degradation.

**Materials and Methods**

Graphene flakes were cleaved from bulk graphite using an adhesive tape. All data shown were collected on natural graphite; we checked that the same behavior is observed also when starting from Kish graphite. Graphene layers of different thickness were transferred onto a heavily doped Si substrate covered by 300-nm-thick, thermally grown SiO$_2$ (9, 32). To identify the layer thicknesses, we used both the analysis of the optical intensity in the green channel from images taken under an optical microscope, as well as Raman spectroscopy (Fig. S1).

To contact the flakes selected for the experiments, electrodes were attached into a Hall bar configuration using conventional microfabrication techniques (electron-beam lithography, electron-beam evaporation, and lift-off). The electrodes consisted of a multilayer Ti/Au/SiO$_2$ (10/50/30 nm), with Ti providing good electrical contacts to graphene and SiO$_2$ minimizing the direct contact area between the electrodes and the ionic liquids (Fig. 1B). The electrical properties of a single graphene device were measured using different ionic liquids under identical conditions, to facilitate a direct comparison of the effect of the different ionic liquids used (we have worked with three different ionic liquids: DEME-TFSI, ABIM-TFSI, and EMIM-BF$_3$).

Because of the sensitivity of the ionic liquid to moisture (humidity present in the liquid can significantly increase the tendency to chemical reactions when the gate voltage is applied), the part of device preparation involving ionic liquid was performed inside a glove box. The devices were then quickly transferred to the vacuum chamber of the measurement system (a cryostat equipped with a 9T magnet) and left under vacuum for several hours before starting the measurements. Experiments were performed by measuring simultaneously the longitudinal and transverse voltage, $V_L$ and $V_T$, as a function of gate voltage bias $V_G$ applied on the Pt gate electrode (Fig. 1B and C, and Fig. S2B). Most transport experiments were repeated several times to ensure the reproducibility of the data, and exclude the occurrence of chemical reactions between the ionic liquid and graphene, which would result in irreversible device degradation.

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**3.** Gonzalez J, Guinea F, Vozmediano MAH (2001) Electron-electron interactions in graphene at high carrier density has been predicted theoretically (3–5). Accessing these higher carrier densities appears to be within experimental reach, by biasing the gate at lower temperature (approximately 220 K, just above the freezing point of the ionic liquid) to prevent the occurrence of chemical reaction (whose rate decreases exponentially with lowering temperature) over a much broader gate voltage range (15, 31).

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